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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

SEP 18 2002

IN RE APPLICATION OF: :

TECHNOLOGY CENTER 2800

KOICHI MOTOIKE : EXAMINER: WELLS, KENNETH B.

SERIAL NO. : 09/841,595 :

FILED: APRIL 25, 2001 : GROUP ART UNIT: 2816

FOR: SEMICONDUCTOR
INTEGRATED CIRCUIT :

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Official Action dated June 17, 2002, please amend the above-identified patent application as follows:

IN THE SPECIFICATION

Please amend the paragraphs in the specification identified below as shown in clean form below. A marked-up copy of amended paragraphs is attached.

Page 1, lines 27-30, please amend this paragraph as follows:

B1
FIG. 1 is an equivalent circuit diagram of an FET. Using this equivalent circuit, the reason why the amount of signal transmission cannot sufficiently be lowered when the FET is in the OFF state will be described below.

Page 2, lines 20-²⁵~~24~~, please amend this paragraph as follows:

B2
It is therefore an object of the present invention to eliminate the aforementioned problems and to provide a semiconductor integrated circuit capable of